

Docket No.: LGS/S-0030A

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Joo-Hyong LEE

Serial No. Divisional Appln. of SN 09/290,891

Filed: September 19, 2001

For: LATCH-UP RESISTANT CMOS STRUCTURE

AUTHORIZATION TO TREAT A REPLY AS INCORPORATING
AN EXTENSION OF TIME UNDER 37 C.F.R. §1.136(a)(3)

Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

The U.S. Patent and Trademark Office is hereby authorized to treat any concurrent or future reply that requires a petition for an extension of time under this paragraph for its timely submission, as incorporating a petition for extension of time for the appropriate length of time under 37 C.F.R. 1.136(a)(3). The U.S. Patent and Trademark Office is hereby authorized to charge all required extension of time fees to our Deposit Account No. 16-0607, if such fees are not otherwise provided for in such reply. A duplicate copy of this sheet is enclosed.

Respectfully submitted,
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